FIG. 1

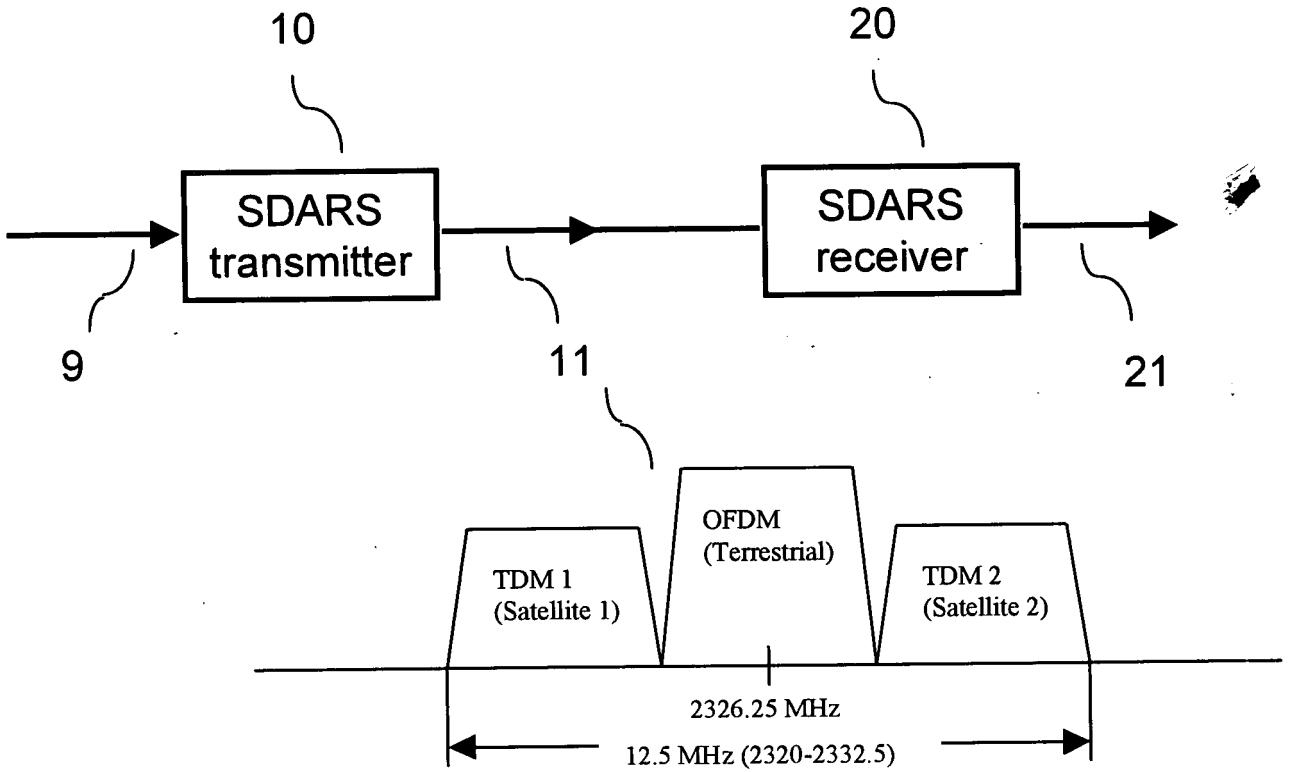

FIG. 2

Cluster Synchronization	Global Control	Multiple Clusters

FIG. 3

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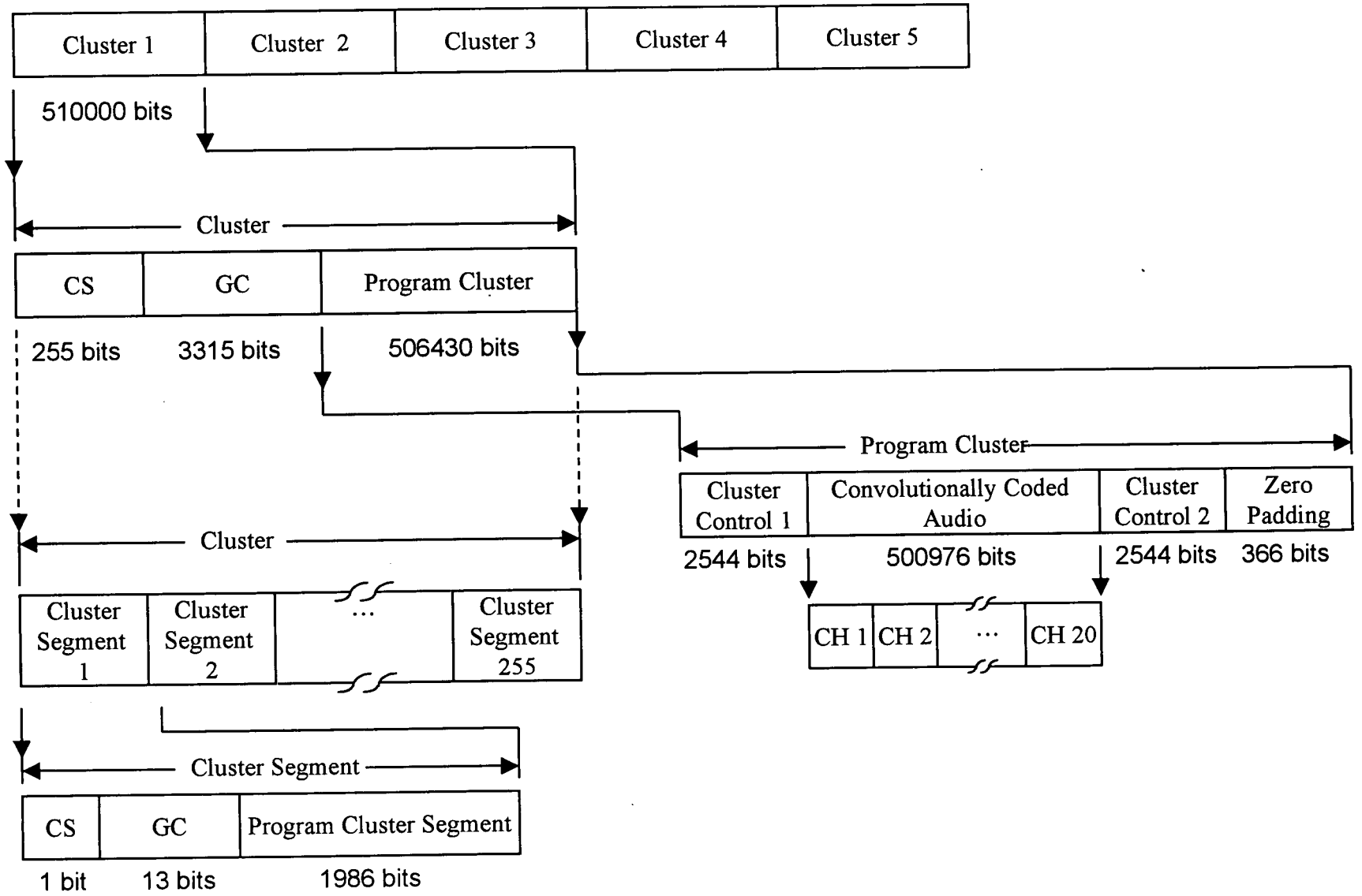


FIG. 4

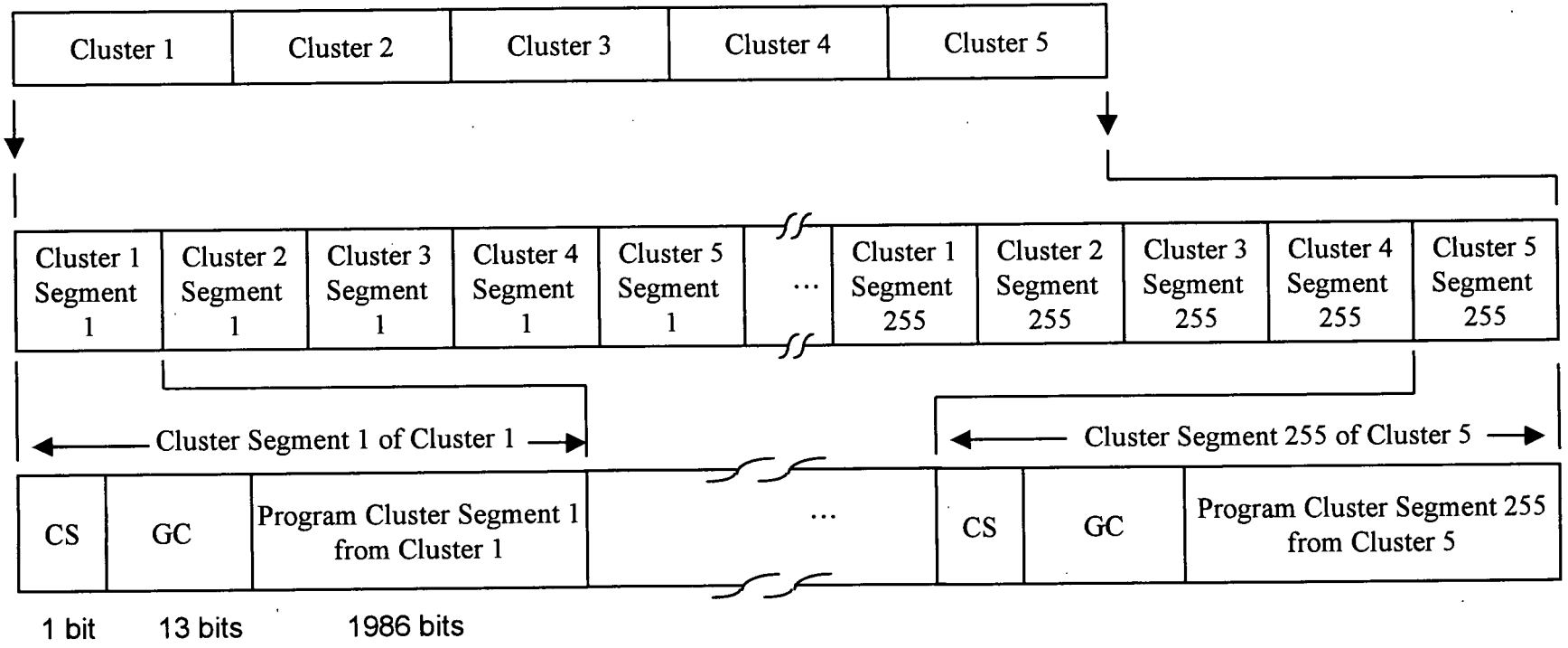

Cluster 1
Cluster 2
Cluster 3
Cluster 4
Cluster 5
Cluster 1 Segment 1
Cluster 2 Segment 1
Cluster 3 Segment 1
Cluster 4 Segment 1
Cluster 5 Segment 1
...
Cluster 1 Segment 255
Cluster 2 Segment 255
Cluster 3 Segment 255
Cluster 4 Segment 255
Cluster 5 Segment 255
Cluster Segment 1 of Cluster 1
Cluster Segment 255 of Cluster 5
CS
GC
Program Cluster Segment 1 from Cluster 1
...
CS
GC
Program Cluster Segment 255 from Cluster 5
1 bit
13 bits
1986 bits

FIG. 5

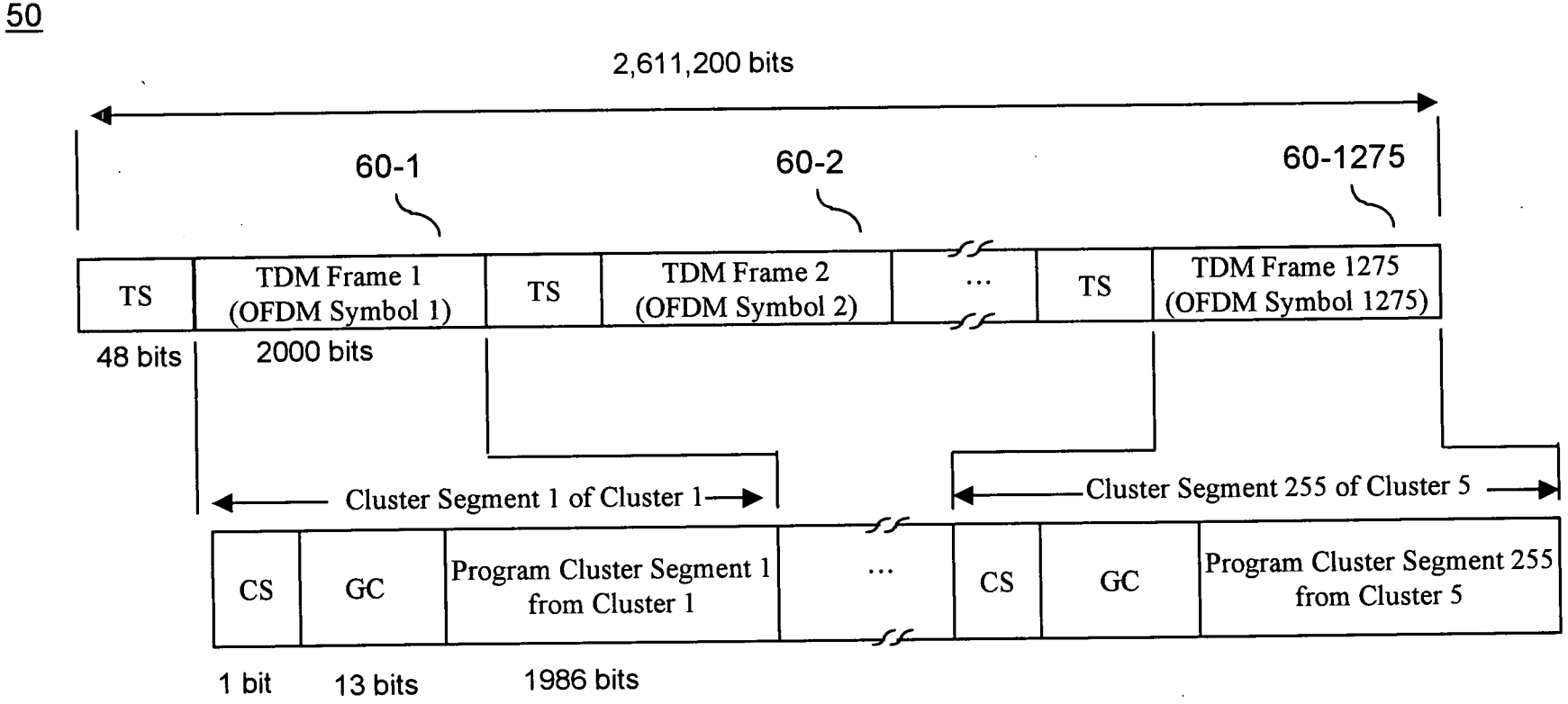
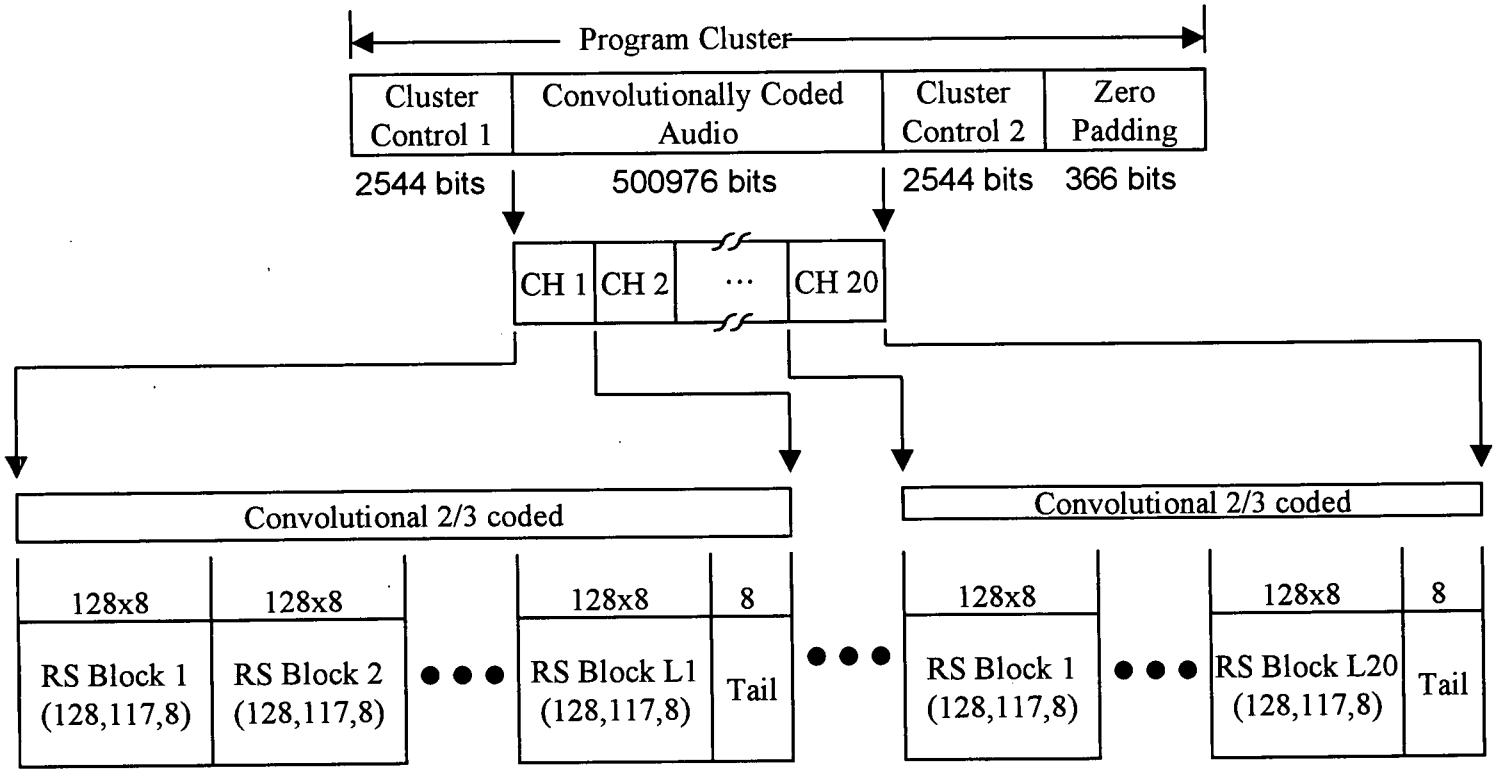


FIG. 6



L1 + L2 + L3 + … + L20 = 326 RS Blocks/Program Cluster,

where, Li = Number of RS blocks for Channel i, $1 \le i \le 20$

the number of RS blocks per channel, Li, is a random variable,
each RS word comprises 8 bits,
concatenated with a 2/3 convolutional encoder,
for each channel, there is a tail comprising 8 bits,

FIG. 7

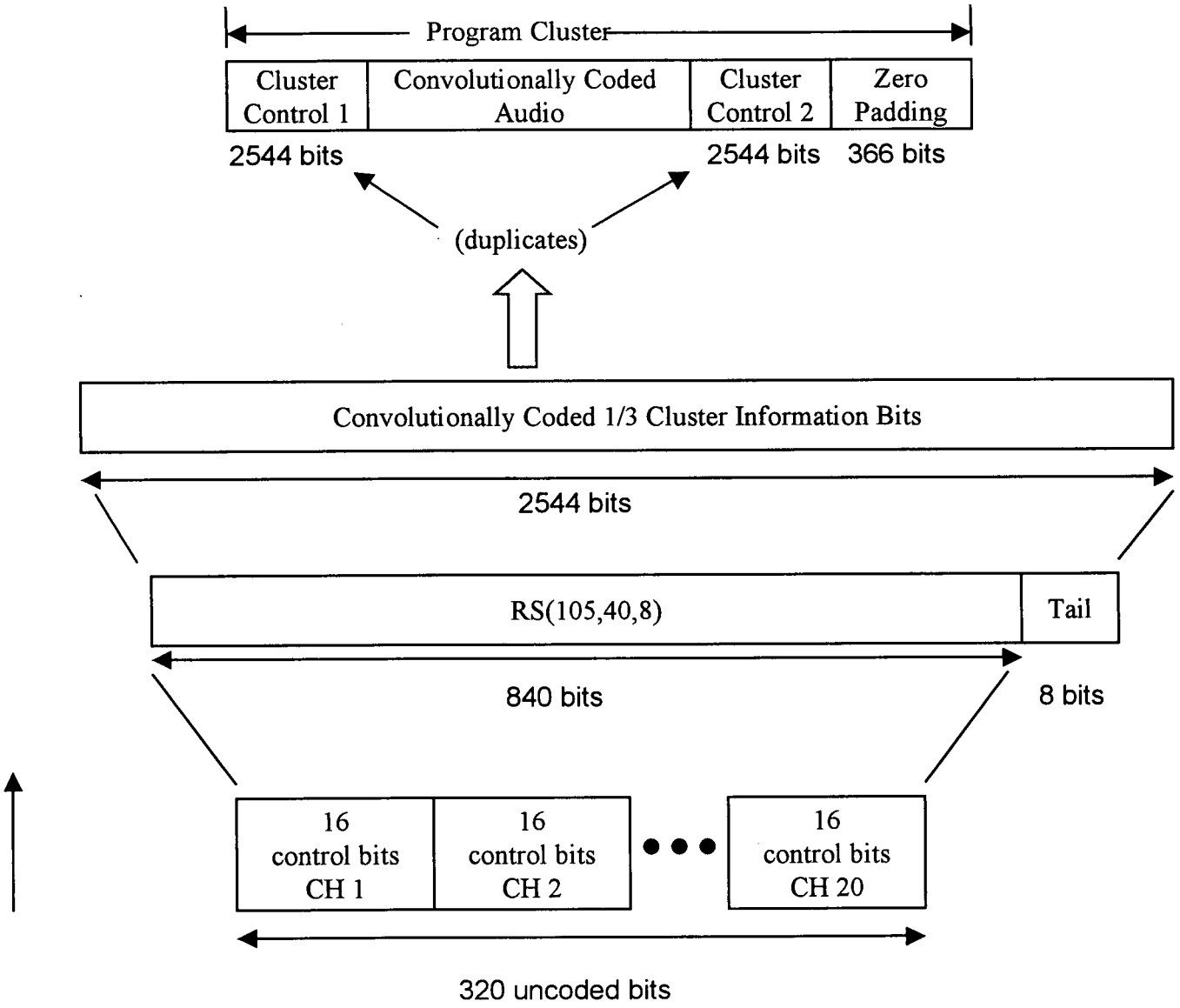

Program Cluster
Cluster Control 1
Convolutionally Coded Audio
Cluster Control 2
Zero Padding
2544 bits
2544 bits
366 bits
(duplicates)
Convolutionally Coded 1/3 Cluster Information Bits
2544 bits
RS(105,40,8)
Tail
840 bits
8 bits
16 control bits CH 1
16 control bits CH 2
16 control bits CH 20
320 uncoded bits

FIG. 8

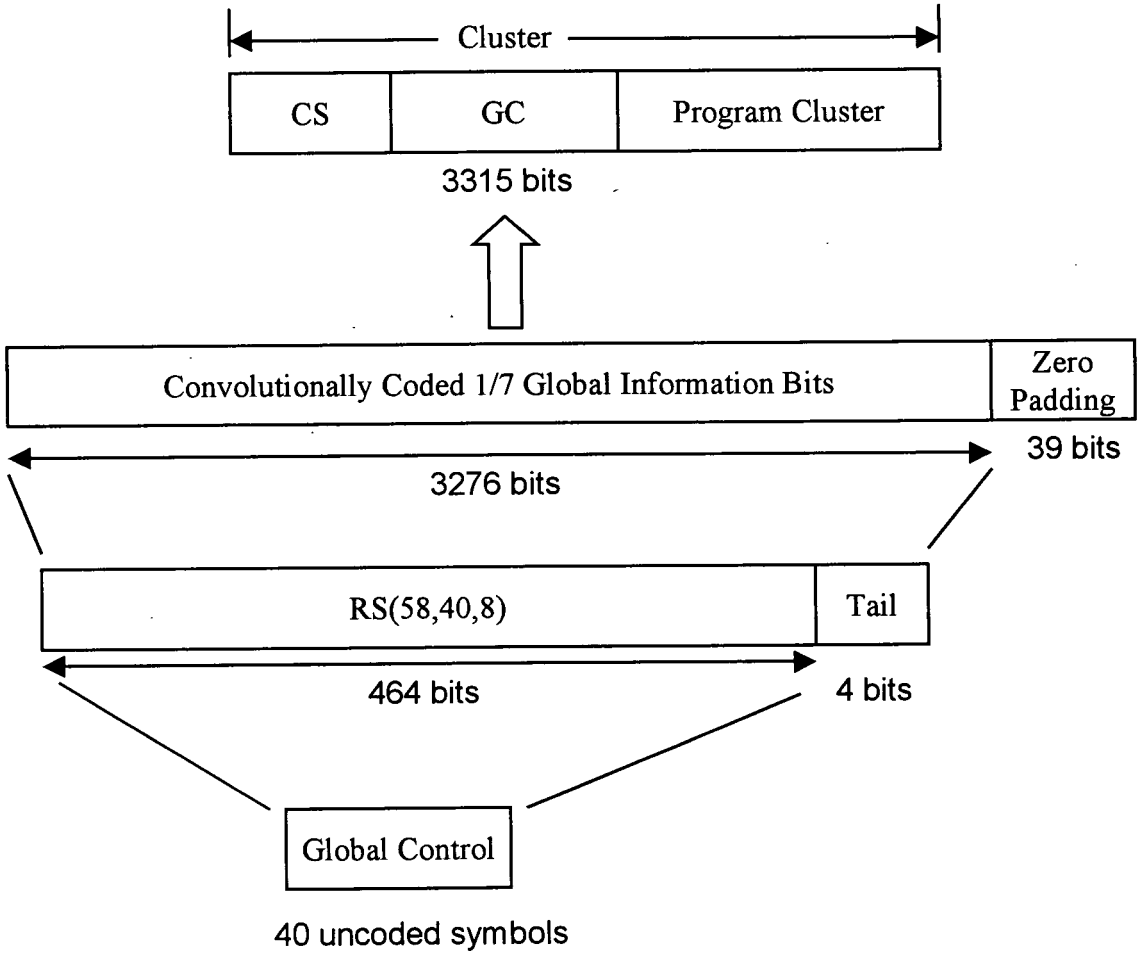

Cluster
CS
GC
Program Cluster
3315 bits
Convolutionally Coded 1/7 Global Information Bits
Zero Padding
39 bits
3276 bits
RS(58,40,8)
Tail
464 bits
4 bits
Global Control
40 uncoded symbols

FIG. 9

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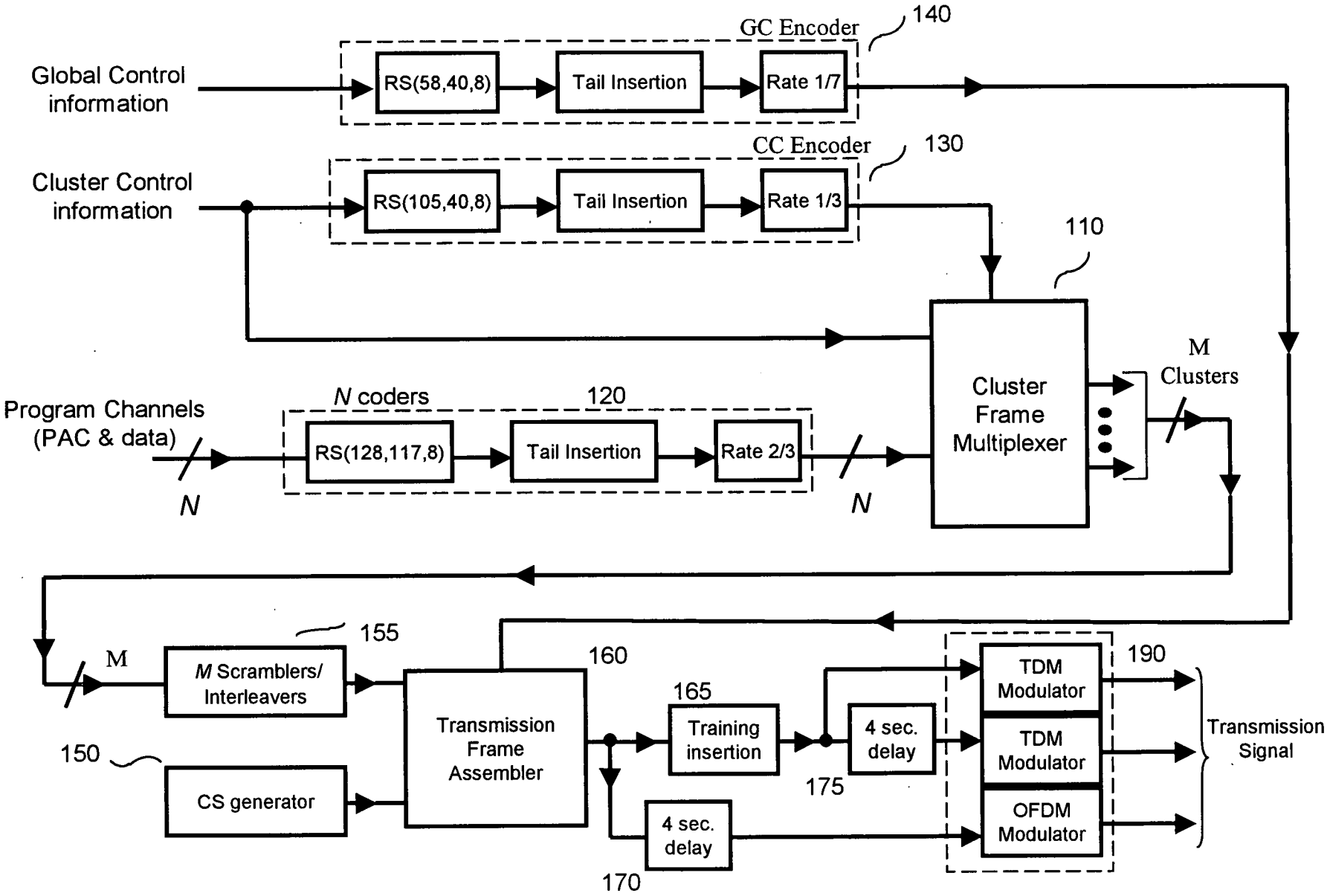
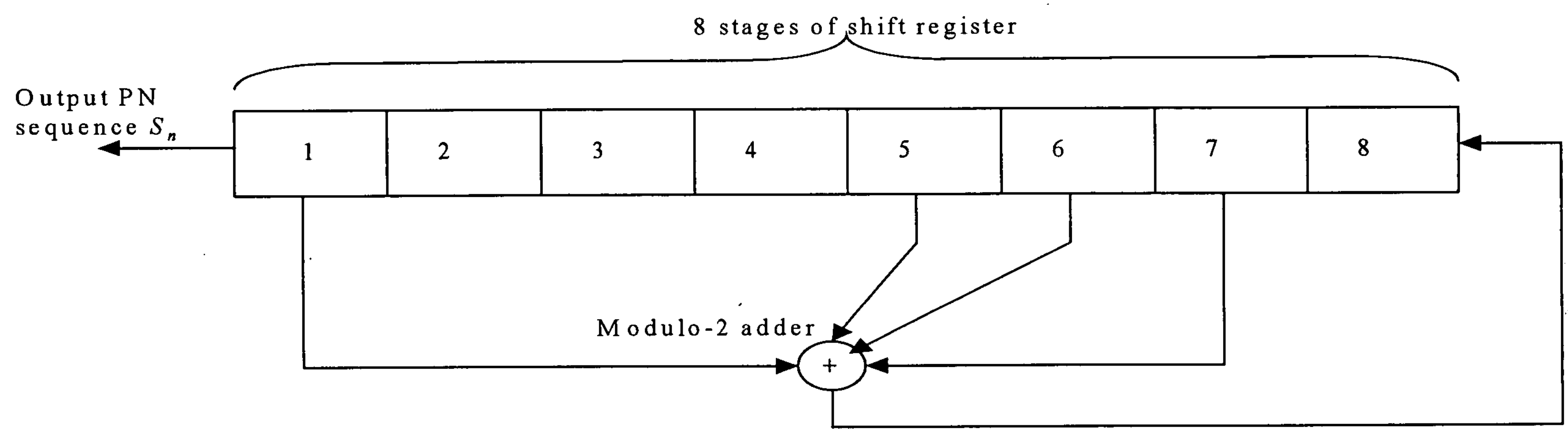


FIG. 10



150

FIG. 11

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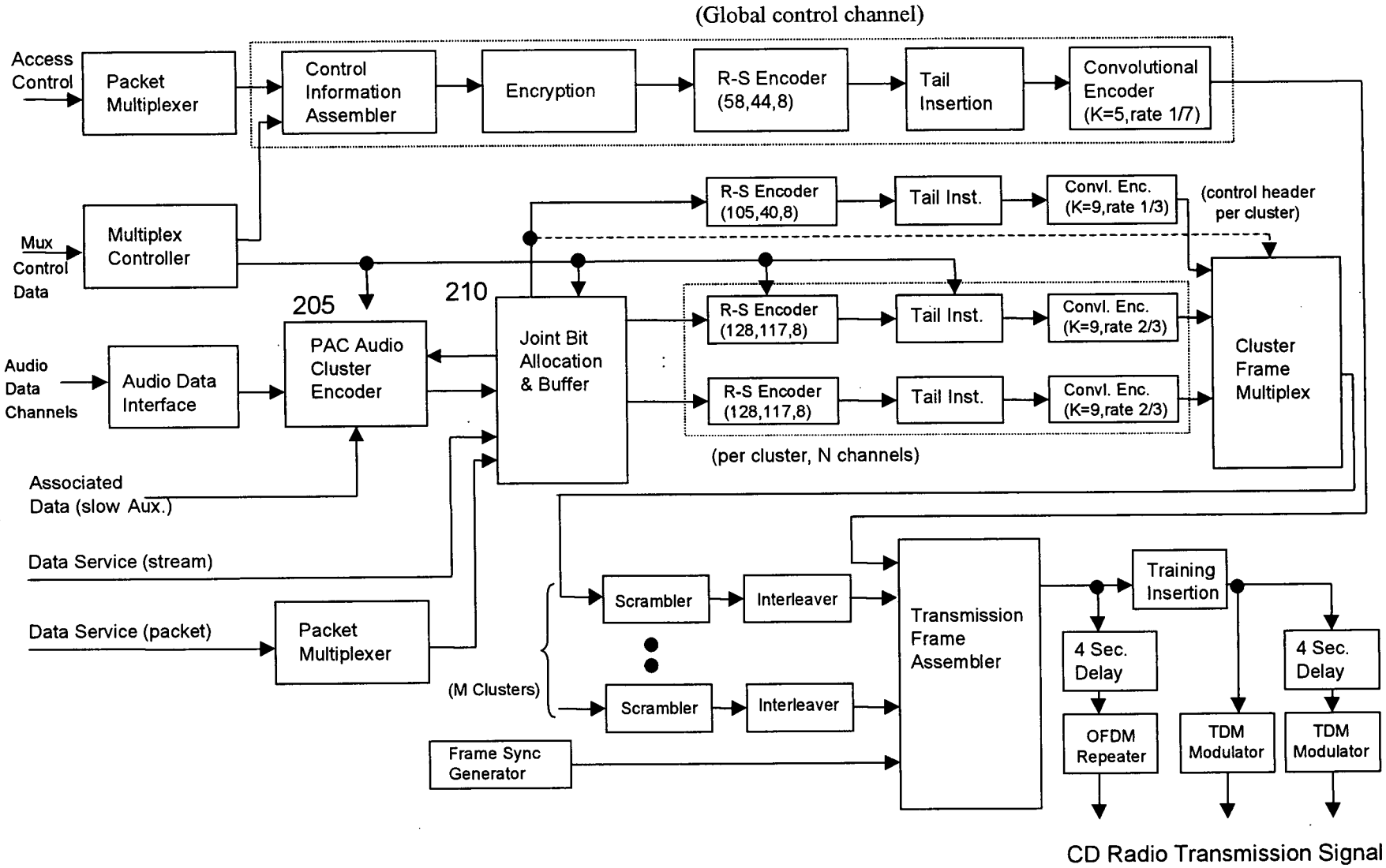

200

FIG. 12

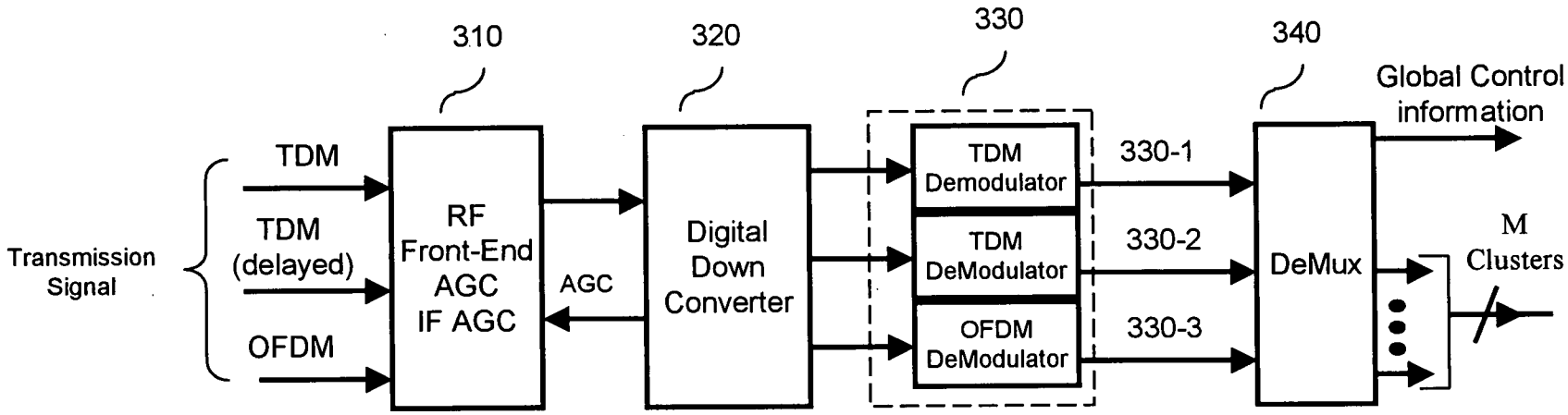
300
310
320
330
340
Global Control information
TDM
TDM (delayed)
OFDM
Transmission Signal
RF Front-End AGC IF AGC
AGC
Digital Down Converter
TDM Demodulator
TDM DeModulator
OFDM DeModulator
330-1
330-2
330-3
DeMux
M Clusters

FIG. 13

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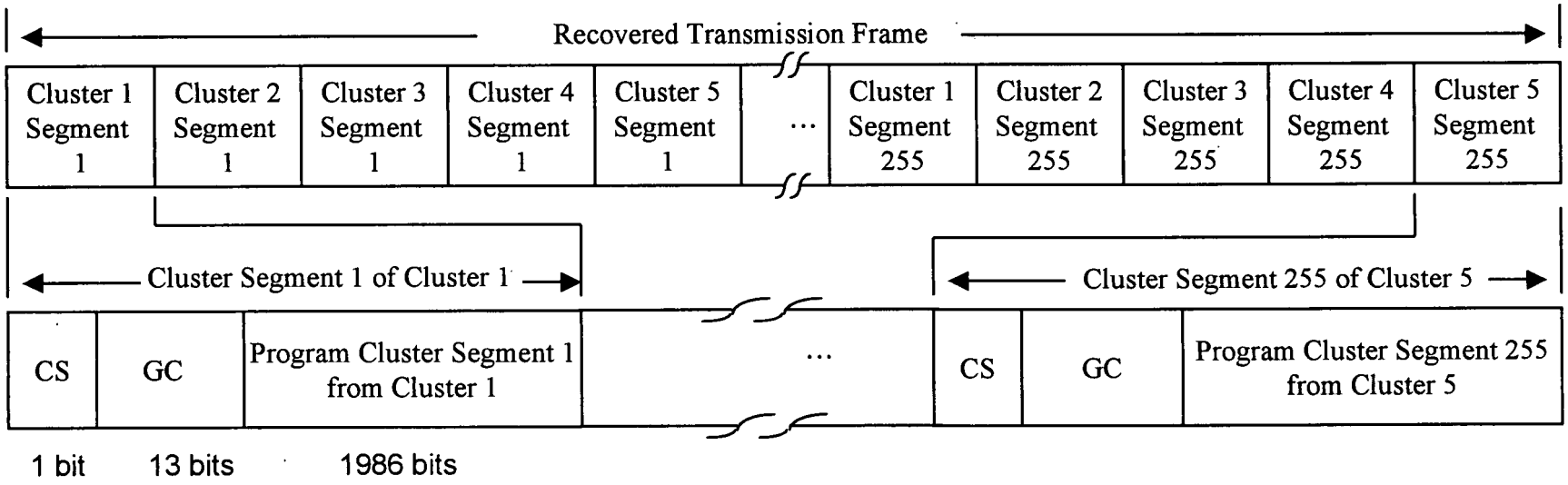

FIG. 14

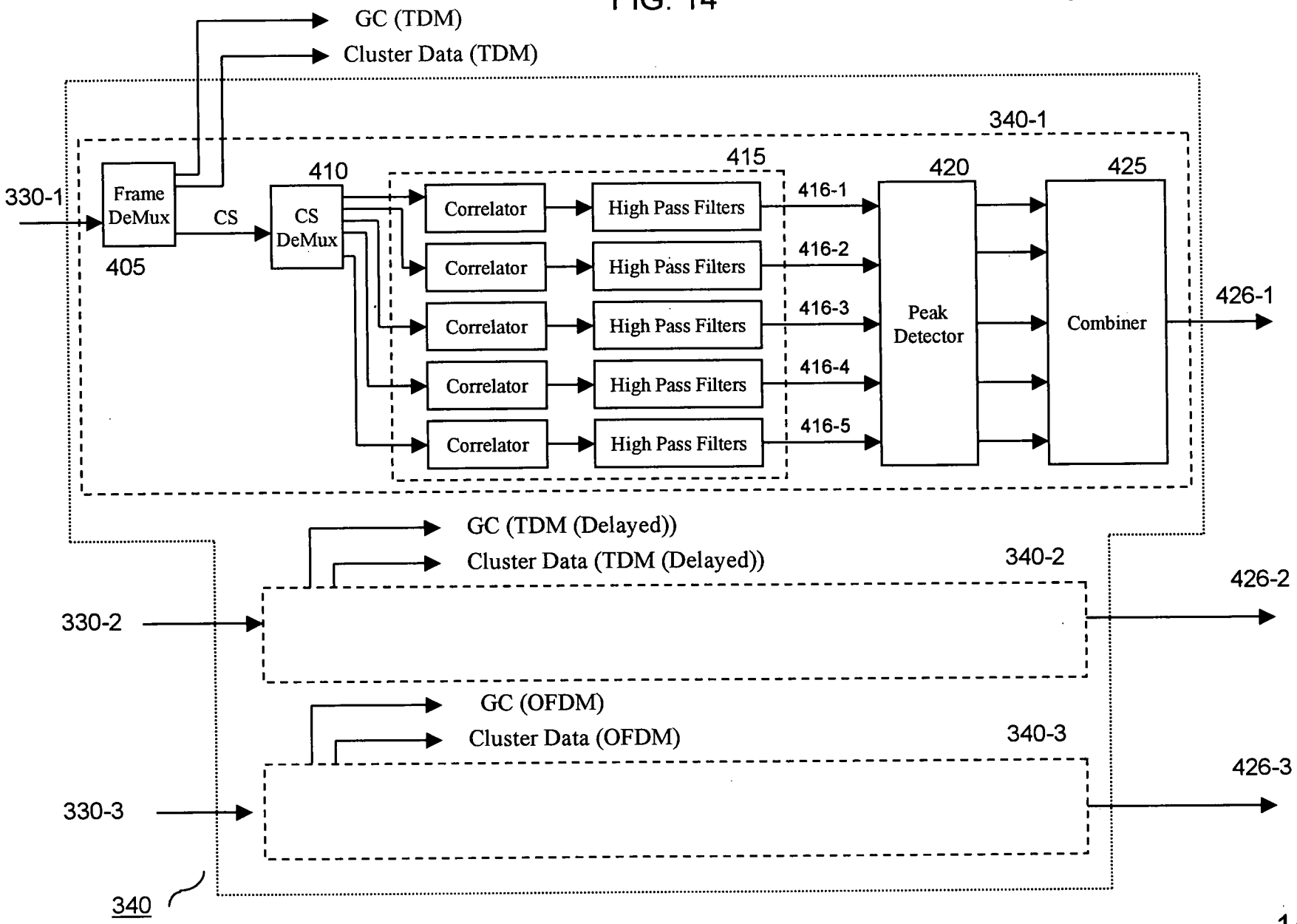
GC (TDM)
Cluster Data (TDM)
340-1
330-1
Frame DeMux
405
CS
410
CS DeMux
415
Correlator
High Pass Filters
416-1
416-2
416-3
416-4
416-5
420
Peak Detector
425
Combiner
426-1
GC (TDM (Delayed))
Cluster Data (TDM (Delayed))
340-2
330-2
426-2
GC (OFDM)
Cluster Data (OFDM)
340-3
330-3
426-3
340

FIG. 15

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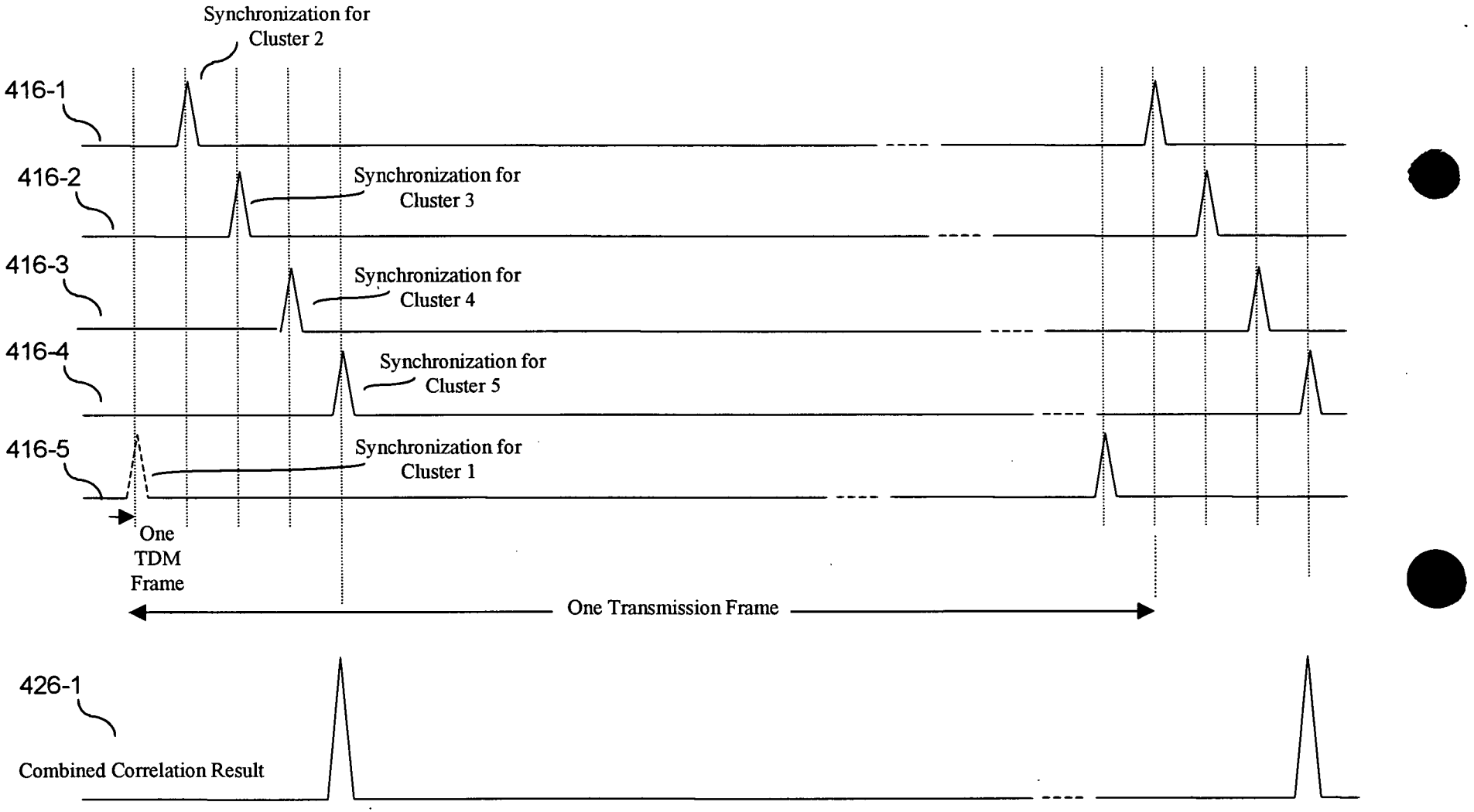
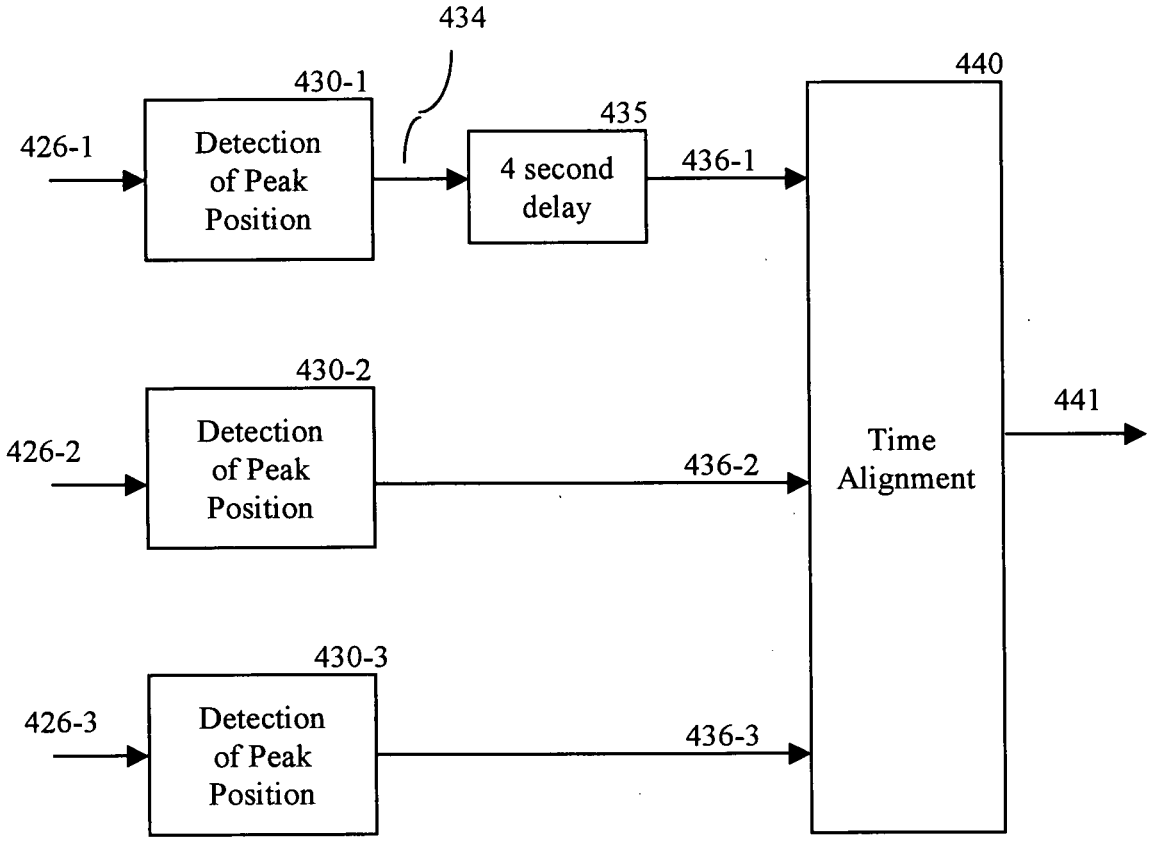
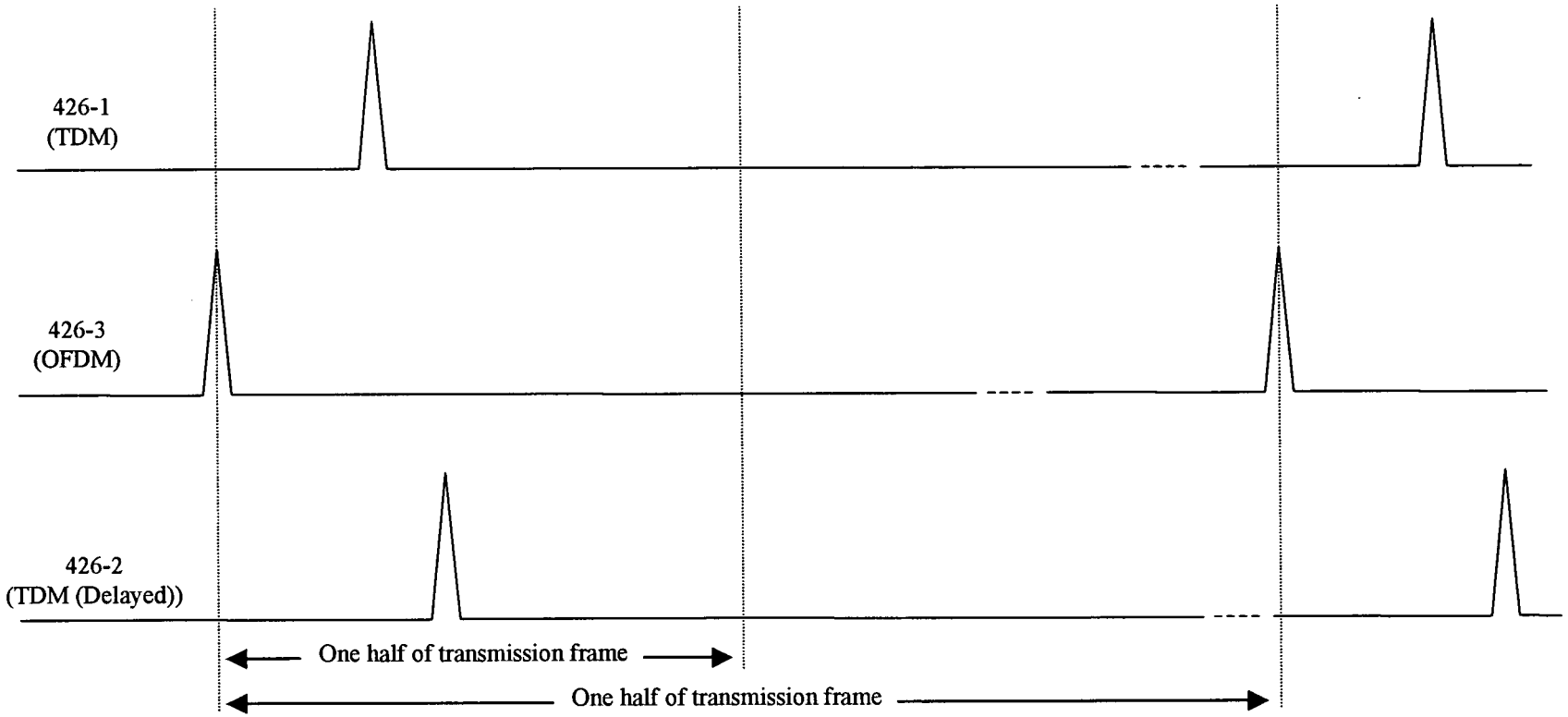


FIG. 16


434
430-1
440
435
426-1
Detection of Peak Position
4 second delay
436-1
430-2
426-2
Detection of Peak Position
436-2
Time Alignment
441
430-3
426-3
Detection of Peak Position
436-3

340

FIG. 17



Note: If differential channel delays are ensure within one half of transmission frame, then cluster synchronization can align three paths in time without ambiguity

FIG. 18

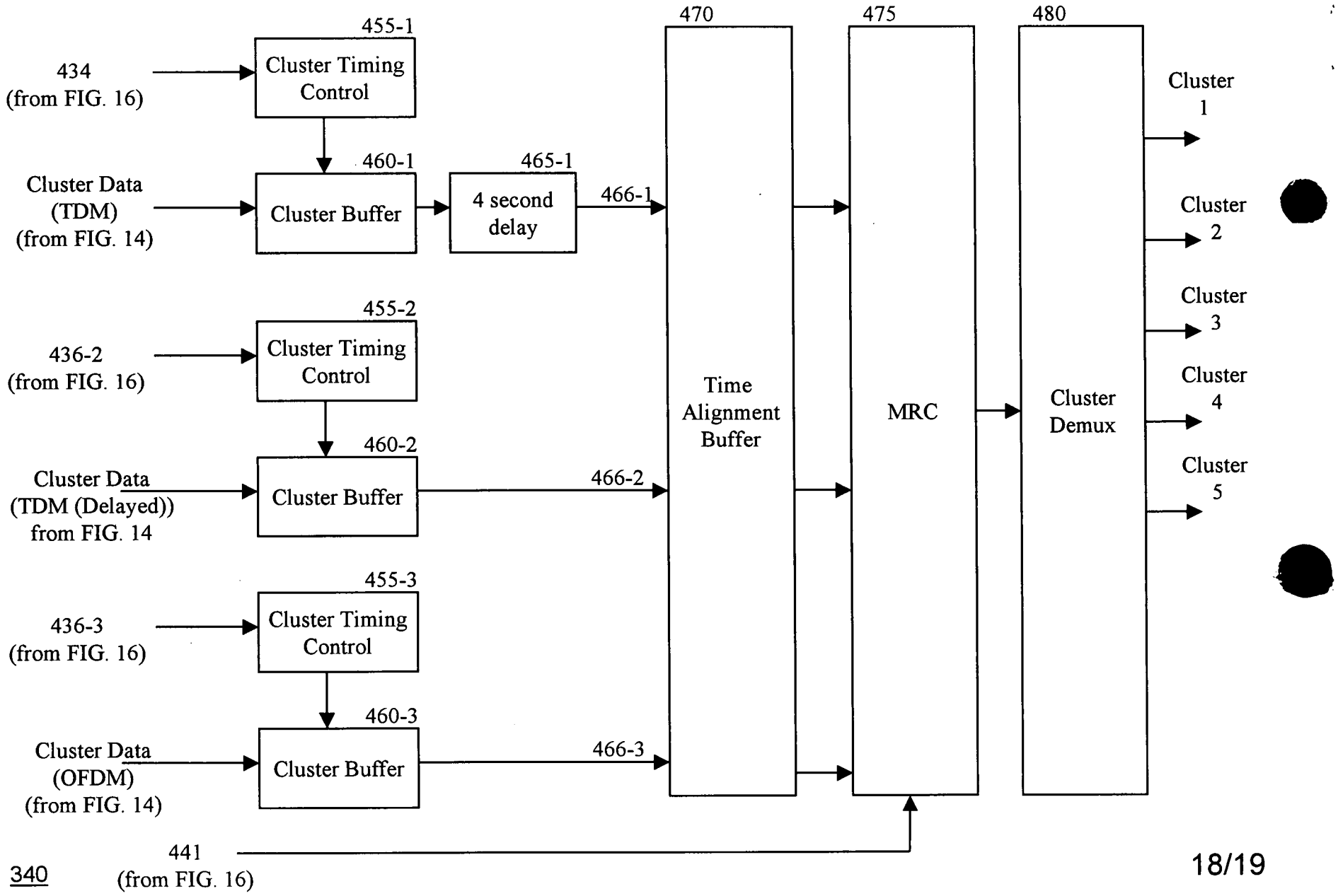
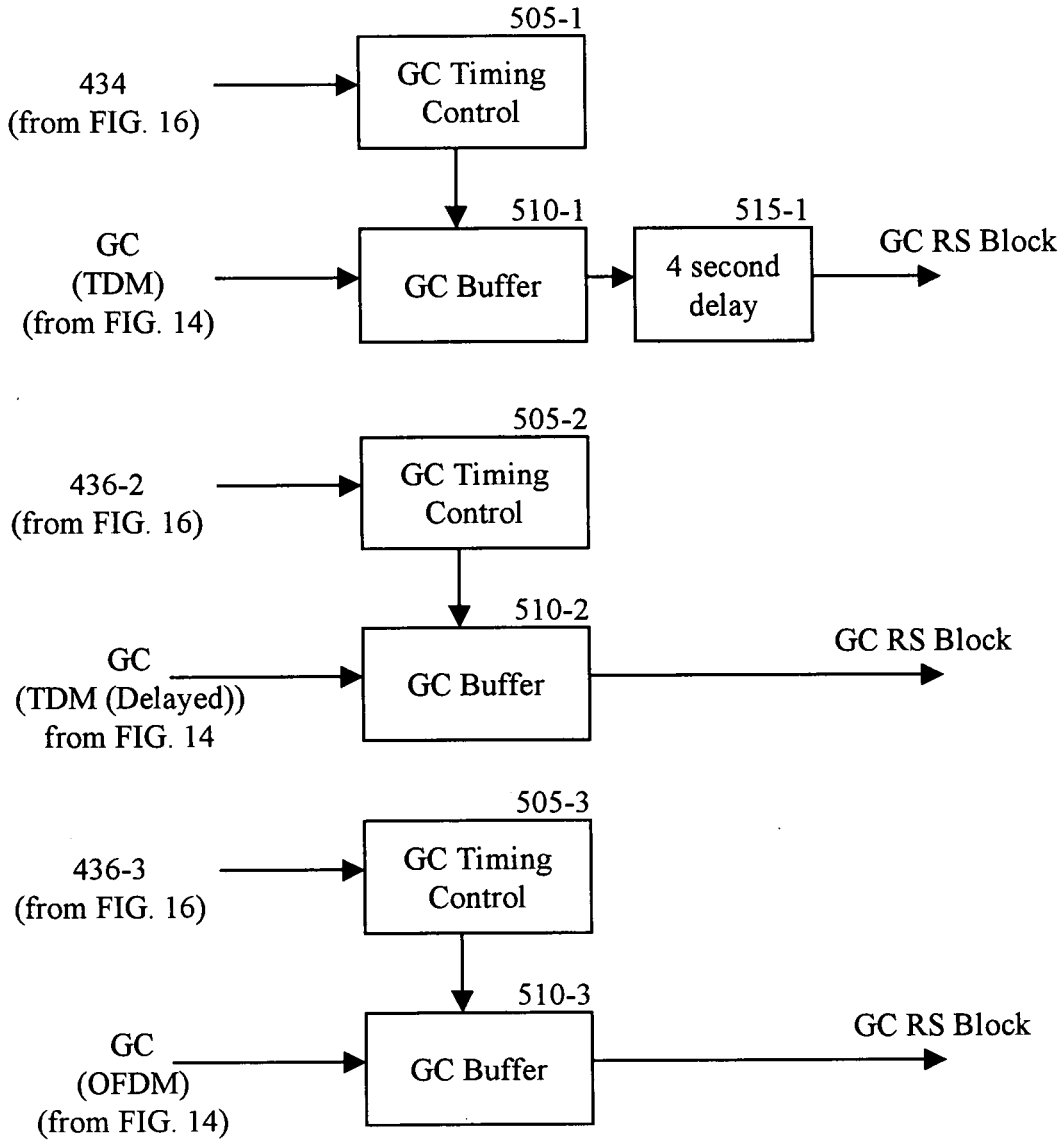

455-1
Cluster Timing Control
434
(from FIG. 16)
460-1
Cluster Buffer
Cluster Data
(TDM)
(from FIG. 14)
465-1
4 second delay
466-1
455-2
Cluster Timing Control
436-2
(from FIG. 16)
460-2
Cluster Buffer
Cluster Data
(TDM (Delayed))
from FIG. 14
466-2
455-3
Cluster Timing Control
436-3
(from FIG. 16)
460-3
Cluster Buffer
Cluster Data
(OFDM)
(from FIG. 14)
466-3
441
(from FIG. 16)
340
470
Time Alignment Buffer
475
MRC
480
Cluster Demux
Cluster 1
Cluster 2
Cluster 3
Cluster 4
Cluster 5

FIG. 19


505-1
434
(from FIG. 16)
GC Timing Control
510-1
515-1
GC
(TDM)
(from FIG. 14)
GC Buffer
4 second delay
GC RS Block
505-2
436-2
(from FIG. 16)
GC Timing Control
510-2
GC
(TDM (Delayed))
from FIG. 14
GC Buffer
GC RS Block
505-3
436-3
(from FIG. 16)
GC Timing Control
510-3
GC
(OFDM)
(from FIG. 14)
GC Buffer
GC RS Block

340